

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yoshihisa MATSUBARA

Serial No. (unknown)

Filed herewith

SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD OF
THE SAME

CLAIM FOR FOREIGN PRIORITY UNDER 35 U.S.C. 119
AND SUBMISSION OF PRIORITY DOCUMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Attached hereto is a certified copy of applicant's corresponding patent application filed in Japan on April 17, 1998 under No. 10-107514.

Applicant herewith claims the benefit of the priority filing date of the above-identified application for the above-entitled U.S. application under the provisions of 35 U.S.C. 119.

Respectfully submitted,

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By

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April 13, 1999

COPY

日 本 国 特 許 庁

PATENT OFFICE
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
this Office.

願 年 月 日
e of Application:

1998年 4月17日

願 番 号
lication Number:

平成10年特許願第107514号

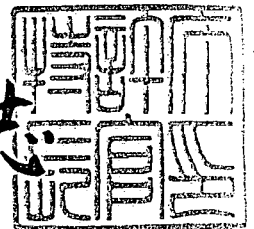
願 人
cant (s):

日本電気株式会社

1999年 2月 5日

特許庁長官
Commissioner,
Patent Office

佐山 建志



出証番号 出証特平11-300368